

668220" 26T29E60

FIG. 2A

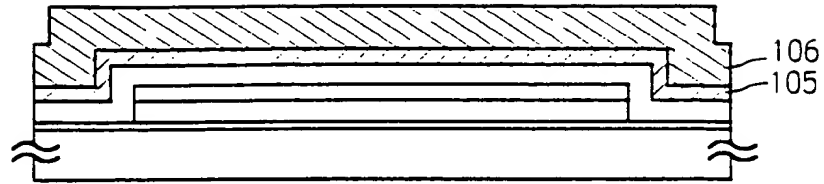


FIG. 2B

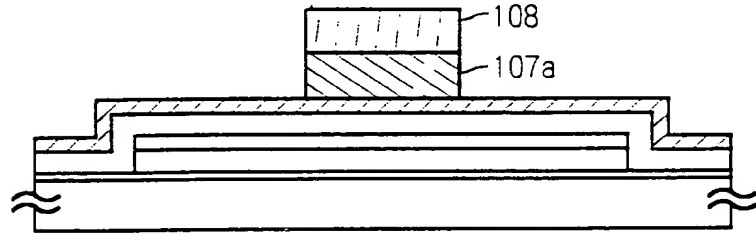


FIG. 2C

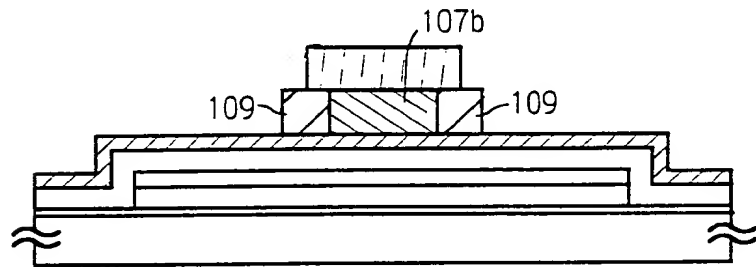


FIG. 2D

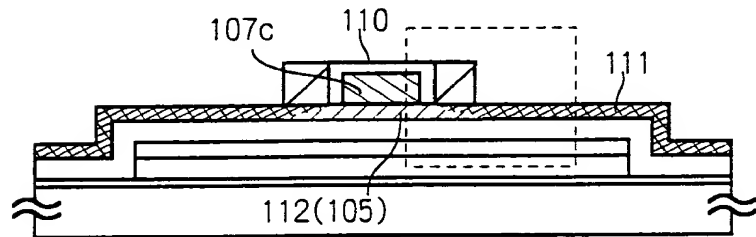


FIG. 2E

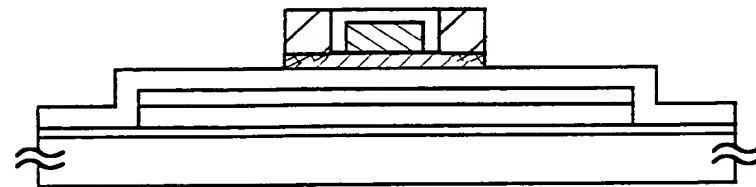


FIG. 3A

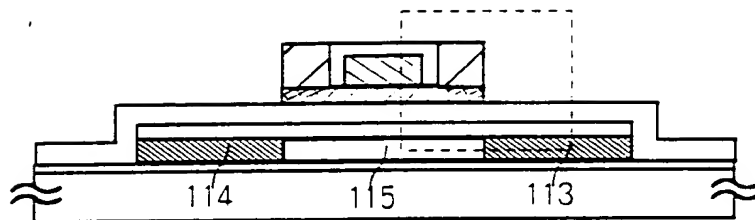


FIG. 3B

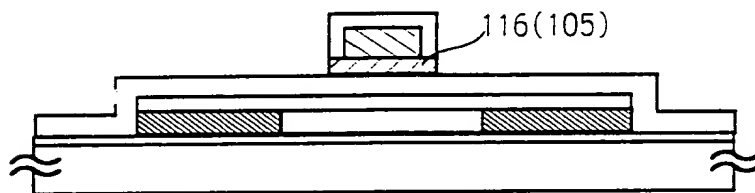


FIG. 3C

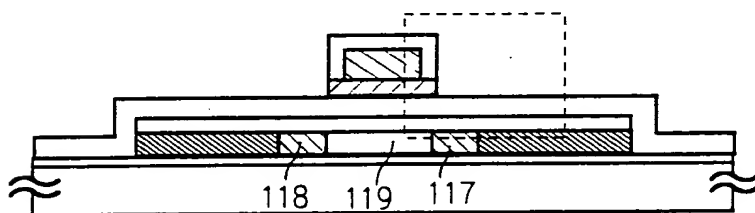


FIG. 3D

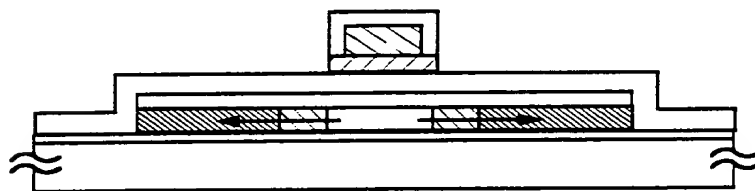
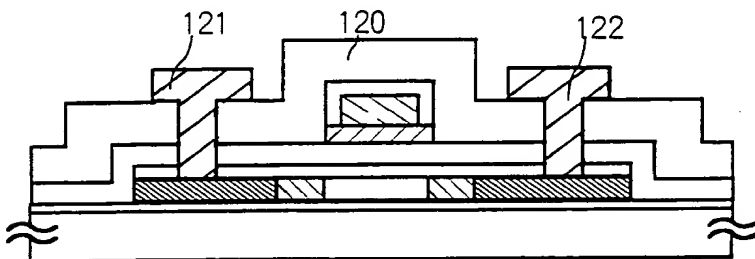


FIG. 3E



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FIG. 4A

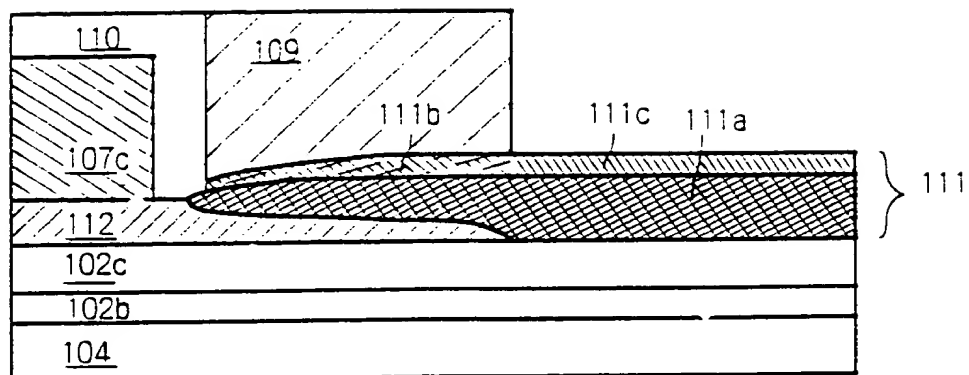


FIG. 4B

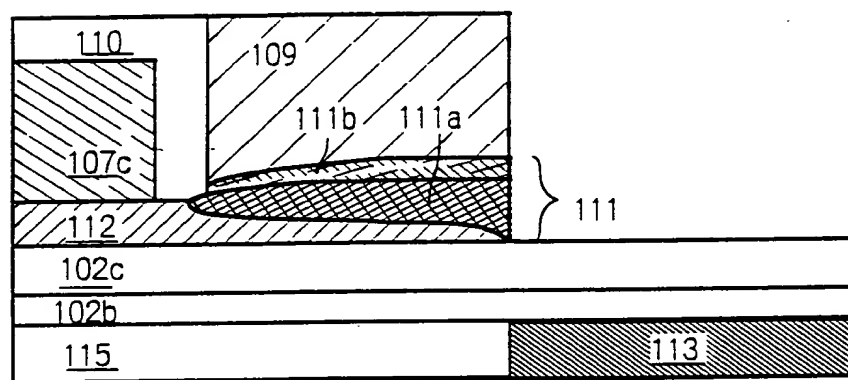
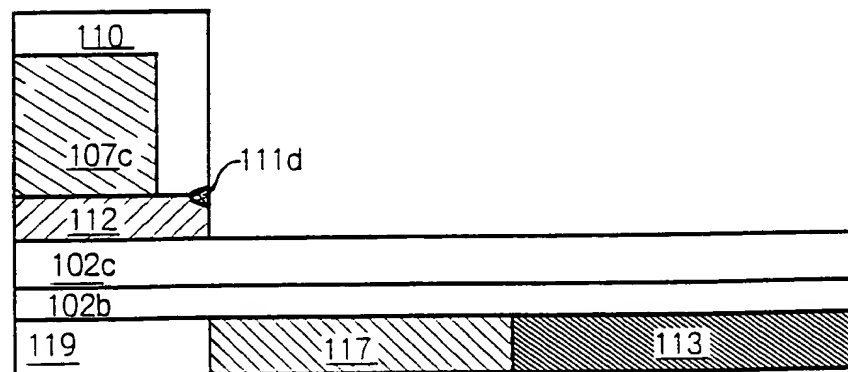


FIG. 4C



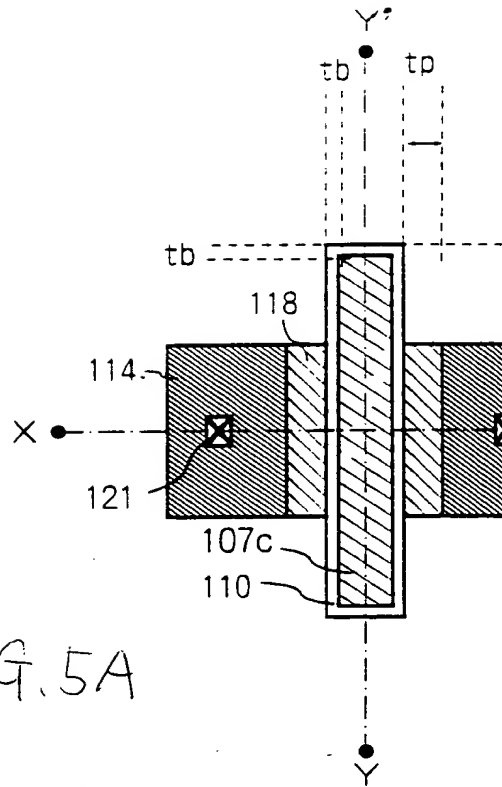


FIG. 5A

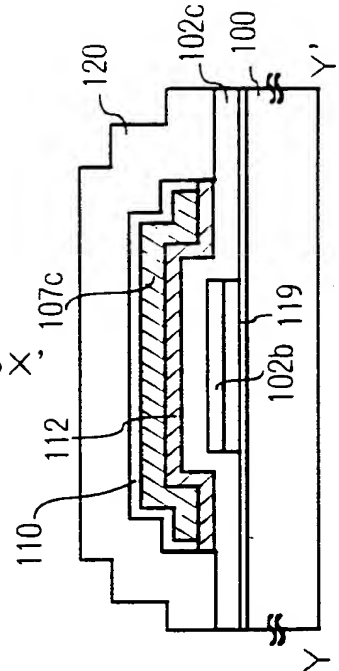


FIG. 5C

Y-Y' SECTION

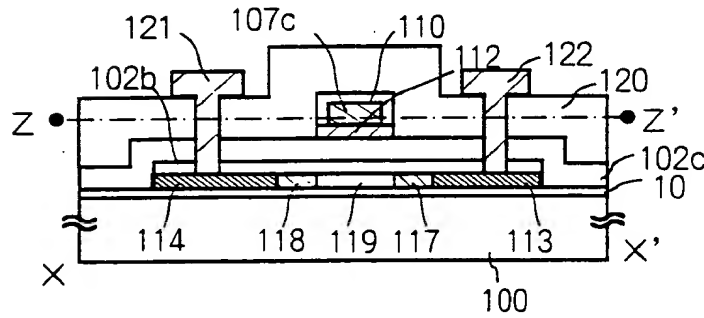


FIG. 5B

X-X' SECTION

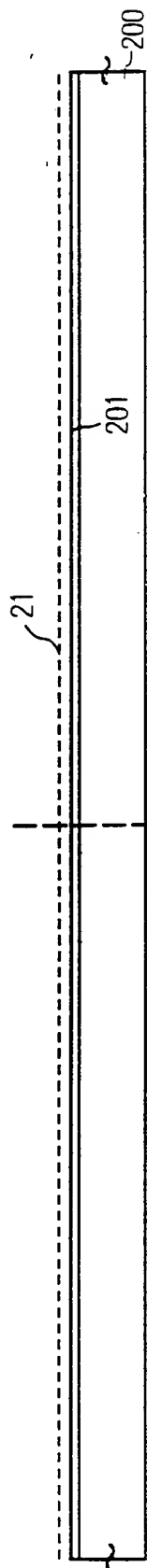


FIG. 6A

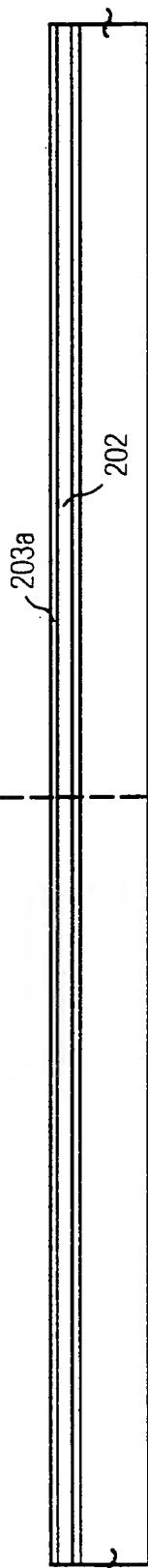


FIG. 6B

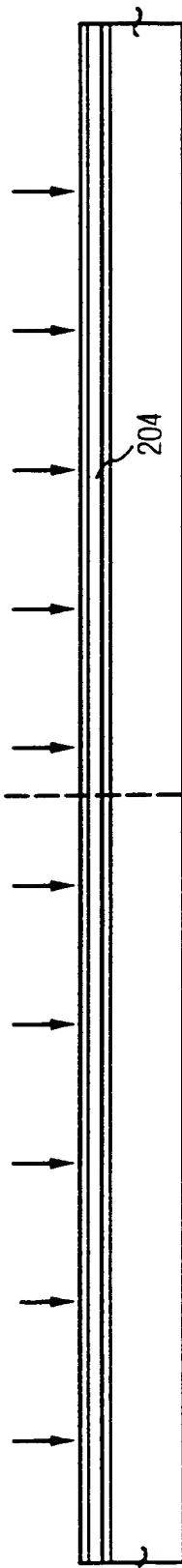


FIG. 6C

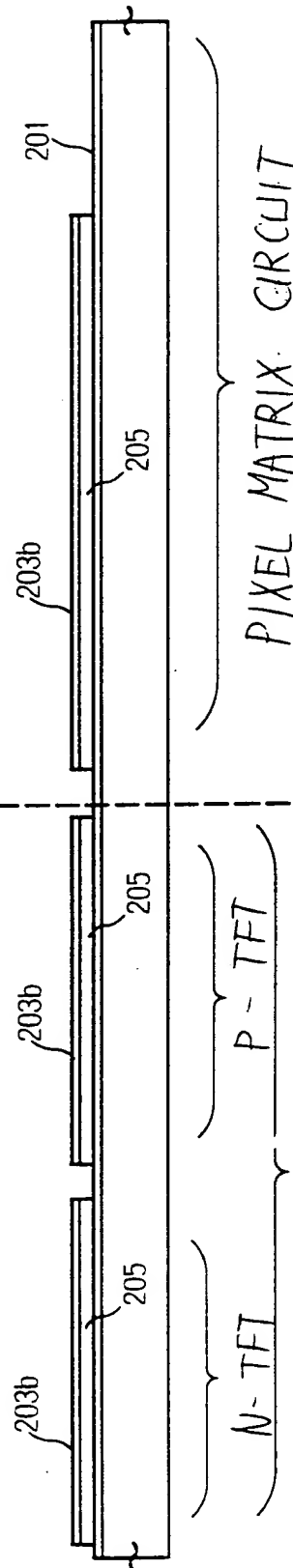


FIG. 6D

PIXEL MATRIX CIRCUIT

A-A' SECTION

CMOS CIRCUIT

B-B' SECTION

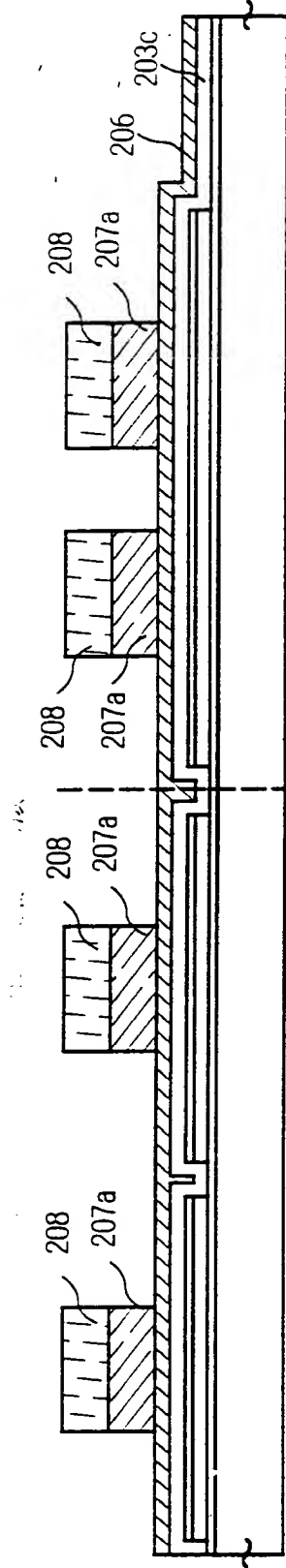


FIG. 7A

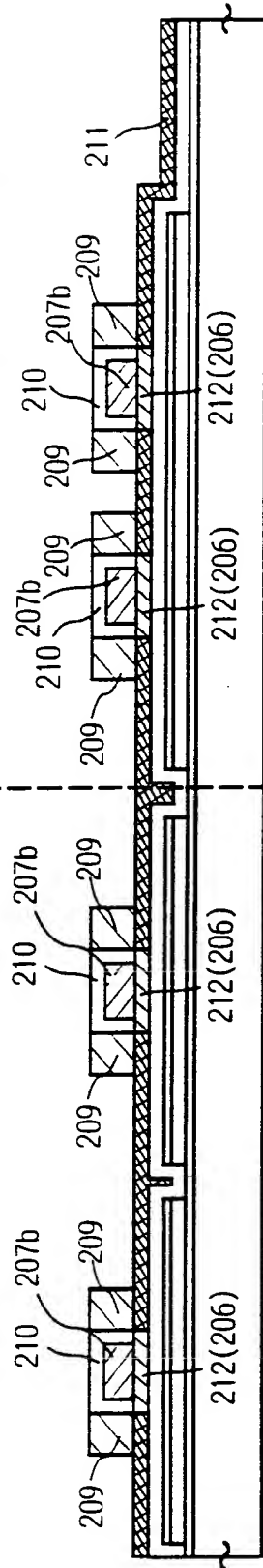


FIG. 7B

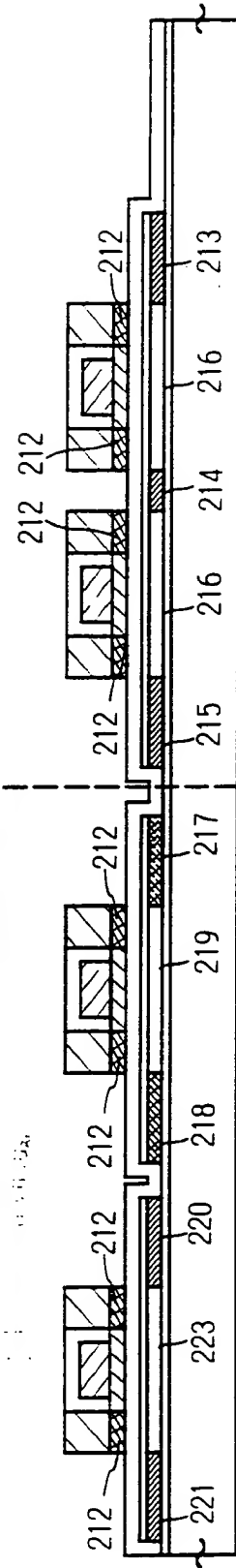


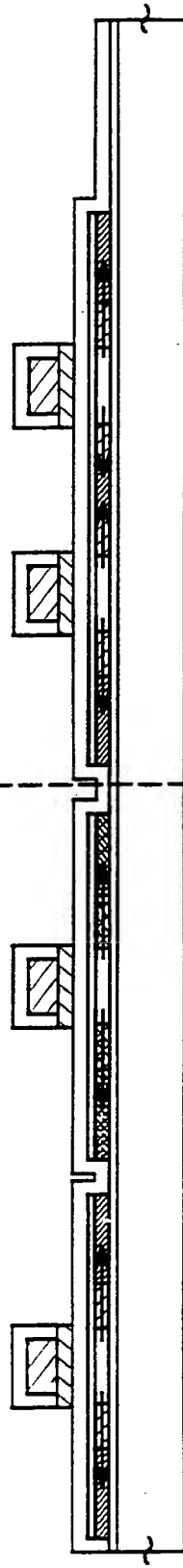
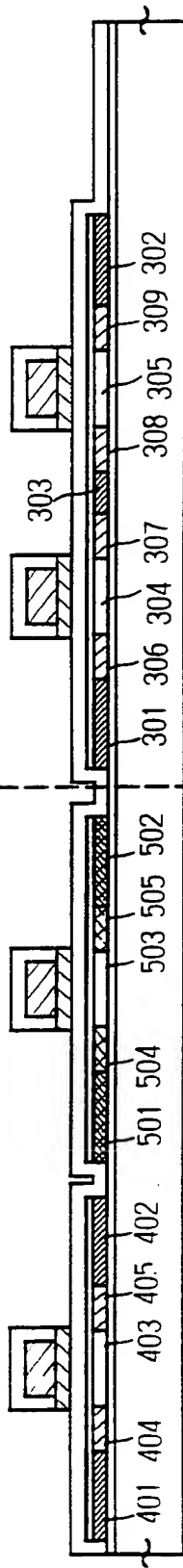
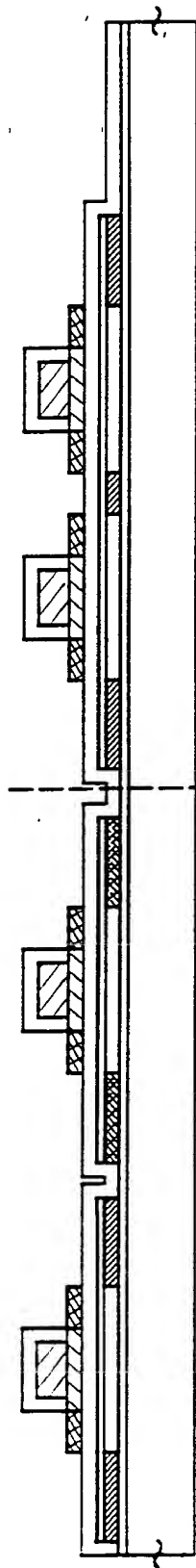
FIG. 7C

PIXEL MATRIX CIRCUIT  
A-A' SECTION

P-TFT

CMOS CIRCUIT  
B-B' SECTION

N-TFT



N-TFT P-TFT  
CMOS CIRCUIT  
B-B' SECTION

PIXEL MATRIX CIRCUIT  
A-A' SECTION



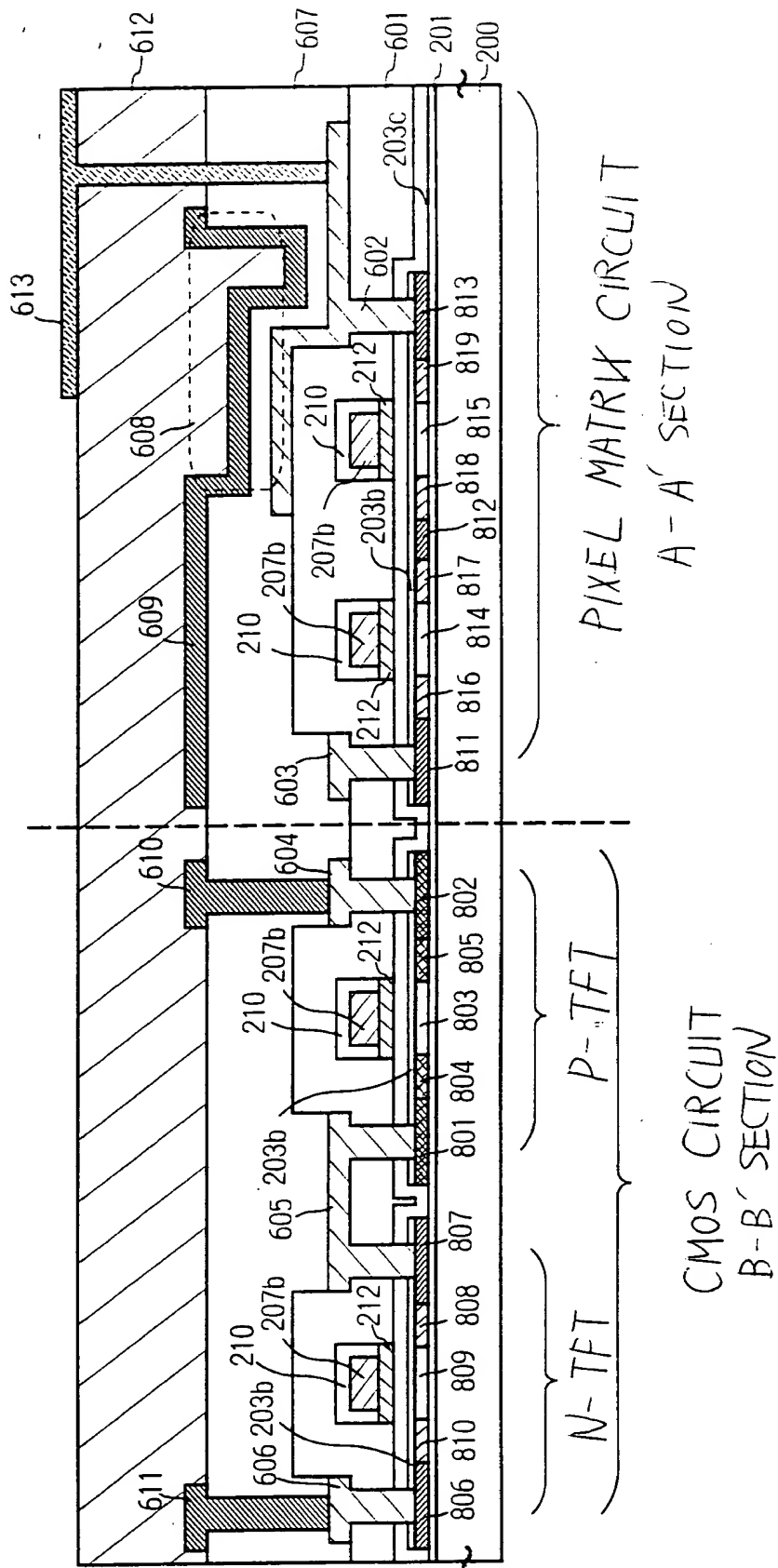


FIG. 9

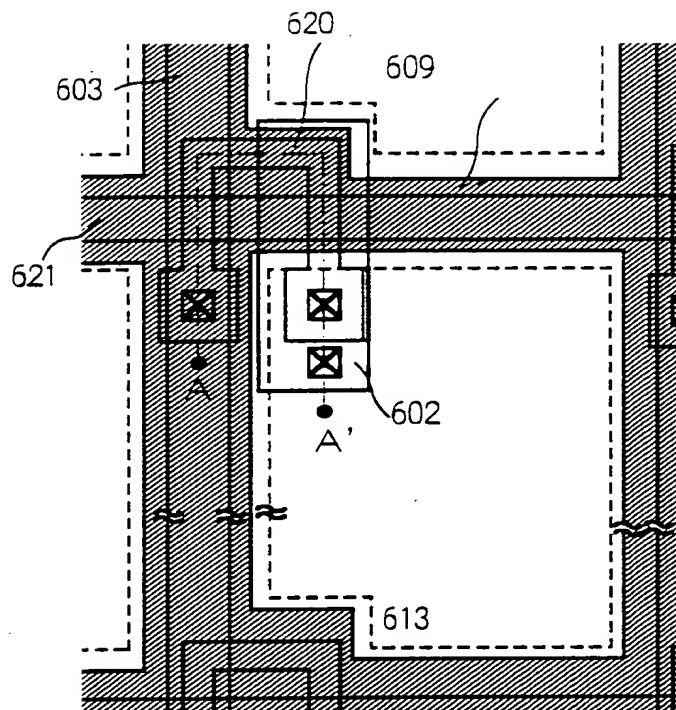


FIG. 10A

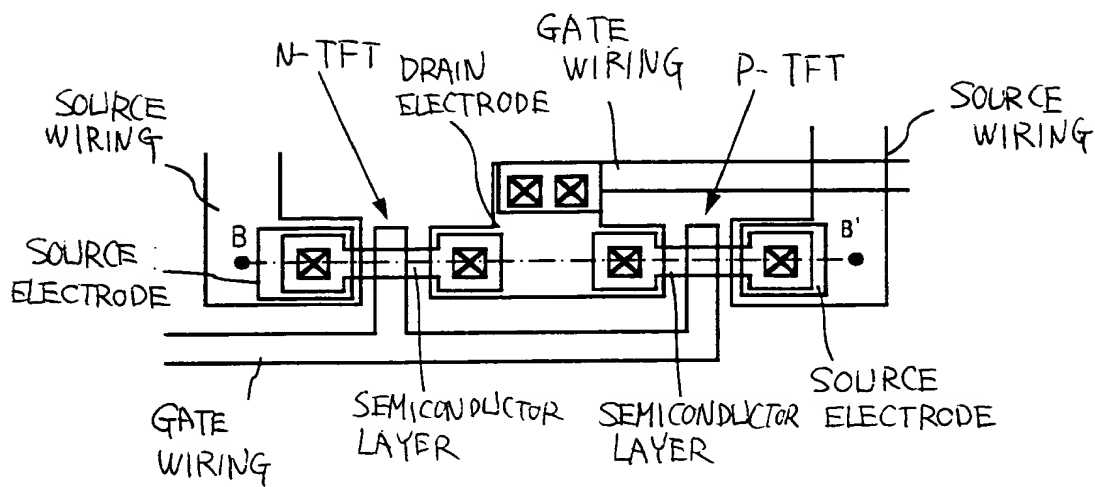
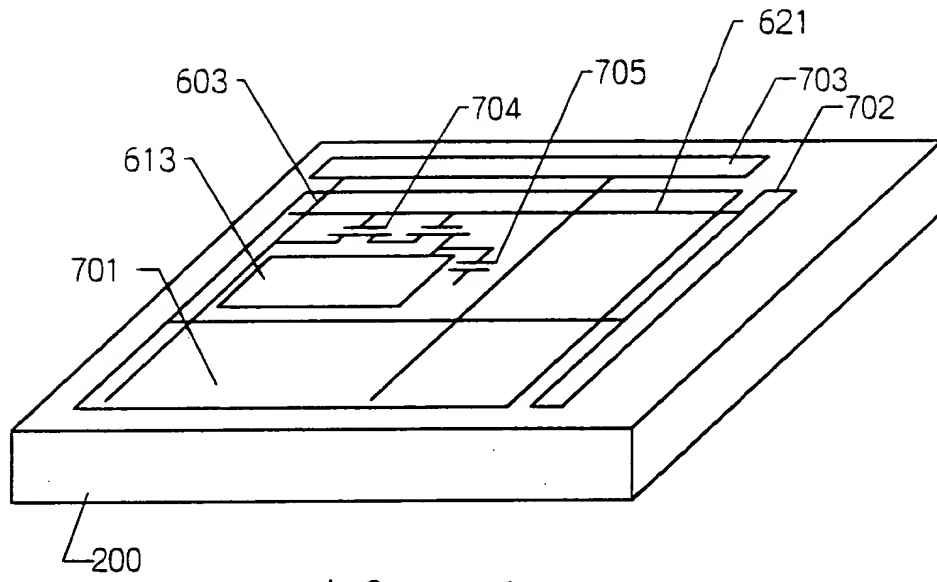


FIG. 10B



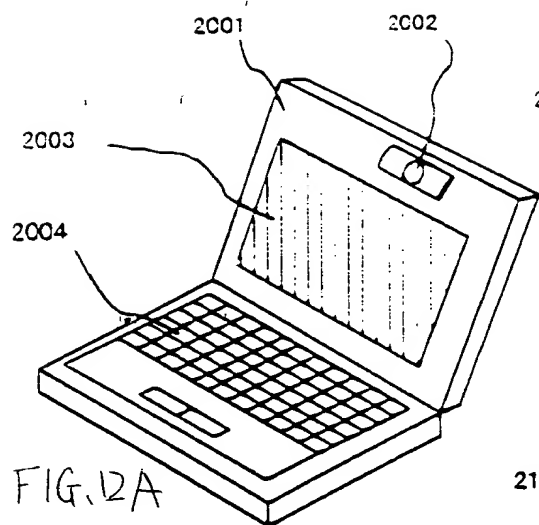


FIG. 12A

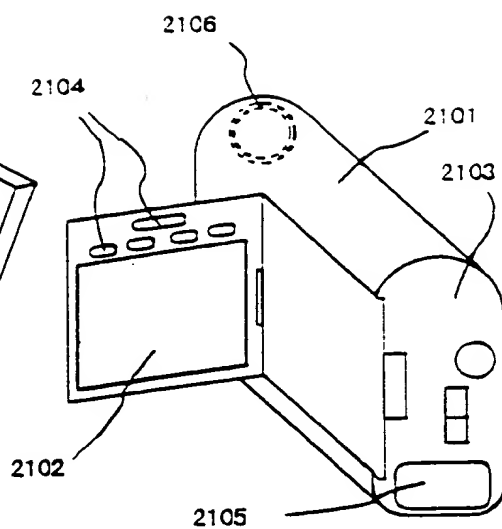


FIG. 12B

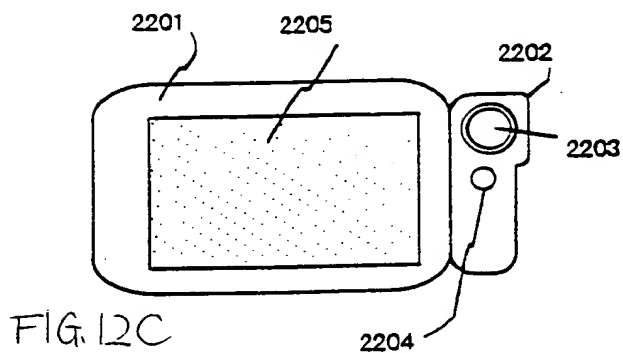


FIG. 12C

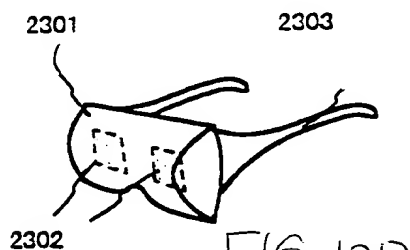


FIG. 12D

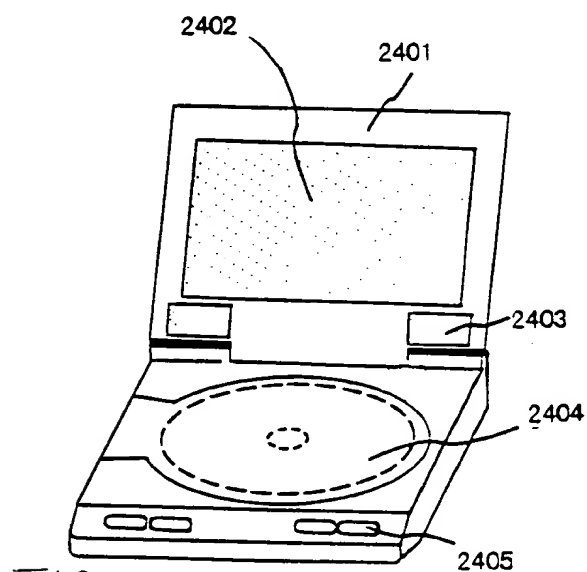


FIG. 12E

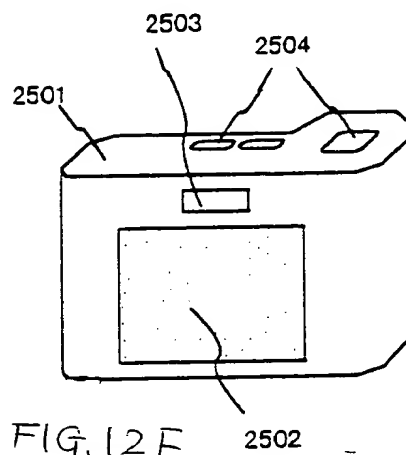


FIG. 12F

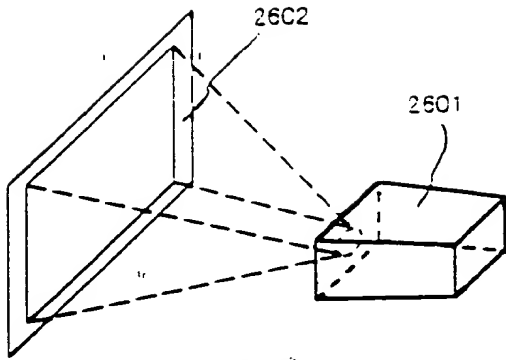


FIG. 13A

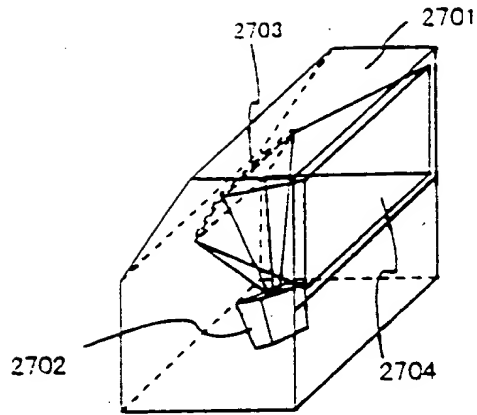


FIG. 13B

SCREEN

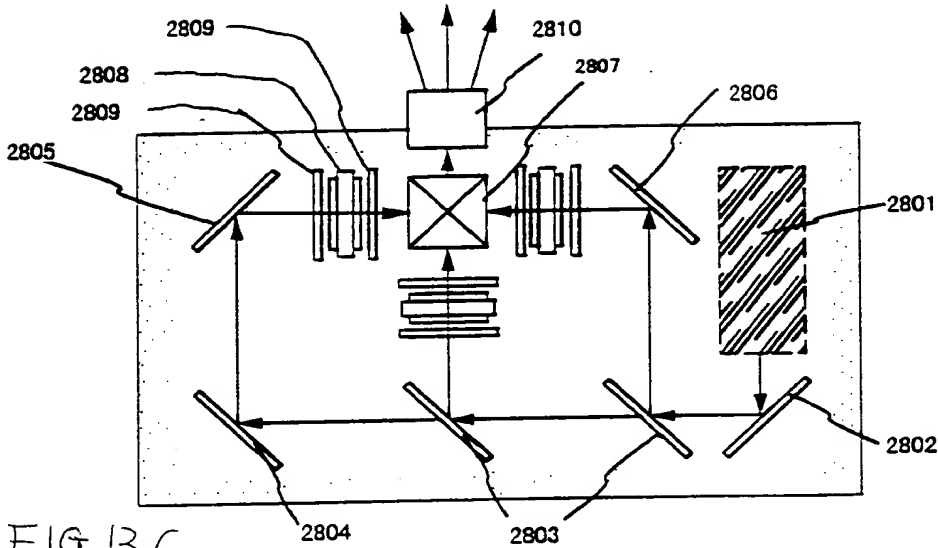


FIG. 13C

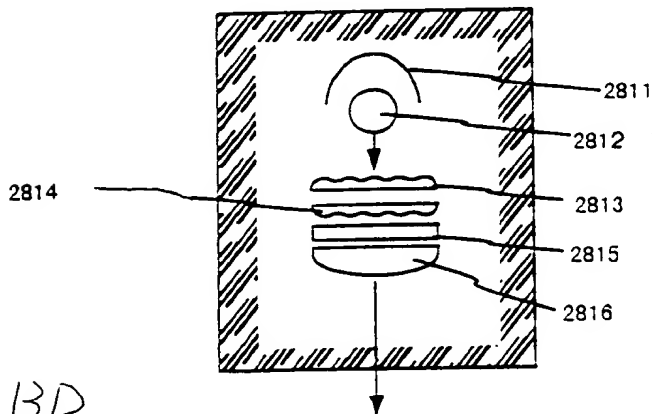


FIG. 13D

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